

a second power supply I/O cell which is not connected to an external pin through a corresponding one of said pads, but receives a power supply from said first power supply I/O cell;

*a!*  
*internal*  
an internal cell; and

a power supply line which provides the power supply to said internal cell, wherein said first power supply I/O cell and said second power supply I/O cell are each connected to said power supply line.

*a*  
10. (Amended) A semiconductor integrated circuit, made by a designing process that comprises:

identifying an I/O cell having no external connection; and  
assigning the identified I/O cell to be a power supply I/O cell having no direct external connection.

Please add the following new Claim 11:

*a*  
3  
--11. (New) A semiconductor integrated circuit, comprising:  
at least one pad;  
a first power supply I/O cell that is connected to an external pin through one of the at least one pad;  
a second power supply I/O cell that is not connected to an external pin through a pad, the second power supply I/O cell being connected to the first power supply I/O cell via a wire line;  
an internal cell; and